

AMENDMENTS TO THE CLAIMS

The following listing of claims replaces all prior listings and versions of claims in this application.

1. (Original) A method for reducing roughness on a free surface of a semiconductor wafer which comprises applying a rapid thermal annealing process under a pure argon atmosphere for a time sufficient to uniformly heat and smooth the free surface of the wafer.

2. (Original) The method of claim 1 which further comprises, prior to conducting rapid thermal annealing, implanting atoms under a face of a donor substrate to form a zone of weakness, bonding a stiffening substrate to the face, and detaching the donor substrate along the zone of weakness to form the wafer including the stiffening substrate and a useful layer.

3. (Original) The method of claim 1 which further comprises rapid thermal annealing at a high temperature dwell in the range of about 1000°C to 1400°C, for a period in the range of about 1 second to 60 seconds.

4. (Original) The method of claim 3 wherein the high temperature dwell is in the range of about 1100°C to 1250°C, for a period in the range of about 5 seconds to 30 seconds.

5. (Original) The method of claim 1 which further comprises polishing the wafer after the rapid thermal annealing.

6. (Original) The method of claim 1 which further comprises implementing at least one sacrificial oxidation stage to reduce slip lines in the free surface of the wafer.

7. (Original) The method of claim 6 wherein the sacrificial oxidation stage is conducted prior to the rapid thermal annealing.

8. (Original) The method of claim 6 wherein the sacrificial oxidation stage is conducted after the rapid thermal annealing.

9. (Original) The method of claim 6 wherein a first sacrificial oxidation stage is conducted prior to the rapid thermal annealing, and a second sacrificial oxidation stage is conducted after the rapid thermal annealing.

10. (Original) The method of claim 9 which further comprises conducting a polishing stage after the rapid thermal annealing and prior to the second sacrificial oxidation stage to further enhance free surface smoothness.

11. (Original) The method of claim 6, wherein the rapid thermal annealing is followed by a first sacrificial oxidation stage, a polishing stage and a second sacrificial oxidation stage to further enhance free surface smoothness after the rapid thermal annealing.

12. (Original) The method of claim 5 which further comprises another rapid thermal annealing stage under pure argon after polishing to further enhance free surface smoothness.

13. (Original) The method of claim 5 which further comprises conducting a first sacrificial oxidation stage prior to the polishing stage.

14. (Original) The method of claim 5 which further comprises conducting a second sacrificial oxidation stage after the polishing stage.

15. (Original) The method of claim 1 which further comprises forming a silicon-on-insulator structure having a free surface with enhanced smoothness.

16. (Original) A method for reducing roughness of a free surface of a wafer of semiconductor material which comprises:

placing a wafer into a chamber;

introducing an annealing atmosphere of pure argon into the chamber at a predetermined pressure;

heating the chamber to increase temperature inside the chamber at a predetermined rate up to a treatment temperature;
maintaining the wafer in the chamber at the treatment temperature for a duration of a high-temperature dwell; and
cooling the wafer at a rate of several tens of degrees Celsius per second.

17. (Original) The method of claim 16 wherein the predetermined pressure is equal to a few millitorr up to atmospheric pressure.

18. (Original) The method of claim 16 wherein the predetermined heating rate is about 50°C per second.

19. (Original) The method of claim 16 which further comprises heating by rapid thermal annealing at a high temperature dwell in the range of about 1000°C to 1400°C for a period in the range of about 1 second to 60 seconds.

20. (Original) The method of claim 16 wherein cooling occurs by means of a flow of air.

21. (New) A method for reducing roughness on a free surface of a semiconductor wafer which comprises:
applying a rapid thermal annealing process under a pure argon atmosphere for a time sufficient to uniformly heat and smooth the free surface of the wafer;
polishing the wafer; and
implementing at least one additional treatment step to reduce any remaining surface defects and enhance smoothness.

22. (New) The method of claim 21, wherein the additional treatment is a sacrificial oxidation stage.

23. (New) The method of claim 22, wherein the sacrificial oxidation stage is conducted after the rapid thermal annealing.

24. (New) The method of claim 22, wherein the sacrificial oxidation stage is conducted prior to the rapid thermal annealing.

25. (New) The method of claim 21, wherein the at least one additional treatment step comprises a first sacrificial oxidation stage conducted prior to the rapid thermal annealing and a second sacrificial oxidation stage conducted after the rapid thermal annealing.

26. (New) The method of claim 21, wherein the at least one additional treatment step comprises conducting another rapid thermal annealing stage under pure argon after polishing to further enhance free surface smoothness.